

R E M A R K S

Careful review and examination of the subject application are noted and appreciated. Applicants' representative thanks Examiner Cox for the indication of allowable subject matter.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 1, 3 and 13-15 under 35 U.S.C. §102 as being anticipated by Fernsler has been obviated by appropriate amendment and should be withdrawn. The allowable matter of claim 9 has been incorporated into claim 1. Matter similar to the allowable matter of claim 9 has been incorporated into claim 13. The allowable matter of claim 20 has been incorporated into claim 14.

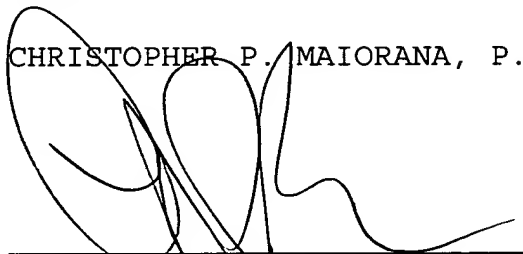
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicants' representative should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge our office
Account No. 50-0541.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

1. (TWICE AMENDED) An apparatus comprising:

a first circuit configured to generate a second reference signal in response to (i) a first reference signal and (ii) a timing signal, wherein (a) a frequency and a phase of said second reference signal are (i) adjusted in response to said first reference signal and (ii) held when said first reference signal is lost and (b) said first reference signal comprises an external timing signal; and

a second circuit configured to generate one or more output signals in response to said second reference signal and one of said one or more output signals, wherein said one or more output signals have a controlled and/or substantially zero delay with respect to said first reference signal.

13. (TWICE AMENDED) An apparatus for generating one or more output signals comprising:

means for generating a second reference signal in response to (i) a first reference signal and (ii) a timing signal, wherein (a) a frequency and a phase of said second reference signal are (i) adjusted in response to said first reference signal and (ii) held when said first reference signal is lost and (b) said first reference signal comprises an external timing signal; and

means for generating said one or more output signals in
10 response to said second reference signal and one of said one or
more output signals, wherein said one or more output signals have
controlled and/or substantially zero delay with respect to said
first reference signal.

14. (TWICE AMENDED) A method of generating one or more
output signals comprising the steps of:

(A) generating a second reference signal in response to
(i) a first reference signal and (ii) a timing signal, wherein (a)
5 a frequency and a phase of said second reference signal are (i)
adjusted in response to said first reference signal and (ii) held
when said first reference signal is lost and (b) said second
reference signal comprises a crystal oscillator signal; and

(B) generating said one or more output signals in
10 response to said second signal and one of said one or more output
signals, wherein said one or more output signals have controlled
and/or substantially zero delay with respect to said second
reference signal.